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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Saksa et al.

Serial No.: 10/085,499

Filed: February 27, 2002

For: Multi-Stage Isolation Barrier
Circuitry and Method for
Connection of a Telephony Device
To a Telephone Line

Attorney Docket No.: 1702-0073
(PU010044)

Examiner: To be assigned

Group Art Unit: To be assigned

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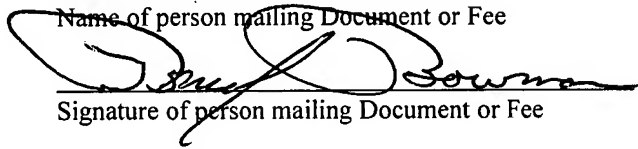
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September 10, 2003

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR § 1.56, Applicant hereby discloses the following references,
copies of which are enclosed, regarding the above-identified patent application.

Patent References

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Issue Date</u>
6,061,445	Rahamim et al.	May 9, 2000
6,198,816	Hein et al.	March 6, 2001
6,201,865	Dupuis et al.	March 13, 2001
6,385,235	Scott et al.	May 7, 2002

<u>Foreign Patent No.</u>	<u>Country</u>	<u>Int'l Publication Date</u>
WO 98/48541	PCT	October 29, 1998

Non-Patent References

1. "3.3 V Global Direct Access Arrangement," Silicon Laboratories, © 2001, (66 pages).
2. Thompson, Mark, "Silicon Laboratories Surges Ahead with Analog Modem Improvements," Silicon Laboratories, Inc, (17 pages).

It is believed that no fees are due for the consideration of this Supplemental Information Disclosure Statement. However, the Commissioner is hereby authorized to charge any deficiency or to credit any overpayment to Deposit Account No. 13-0014, but not to include any payment of issue fees.

Respectfully Submitted,



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September 10, 2003
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